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1 **FLAT PANEL DISPLAY AND A METHOD OF FABRICATION**

2 **THEREFOR**

3

4 **Background of the Invention**

5

6 **Field of the Invention**

7

8

9 The present invention relates generally to display devices for beam steering and
10 scanning and, more particularly, to flat panel display devices for beam steering and
11 scanning which are electronic in character and which further incorporate logic trees which
12 are designed to steer electromagnetic energy so that both transmission losses and the
13 number of energizing sources of an imaging array are minimized. The devices include a
14 multi-stage imaging array made up of a plurality of stacked logic trees so arranged that
15 each stage of each logic tree includes active steering elements which access twice as
16 many like steering elements and associated passive steering elements in each succeeding
17 stage. The active and passive elements incorporate cholesteric liquid crystal (CLC)
18 elements which are polarization sensitive disposed within them at an angle of 45°. The
19 active elements include variable half-wave retarders which, under control of a
20 programmable pulsed source change the polarization of incident energy on CLC elements
21 and provide a scanned line of electromagnetic energy to the imaging cells disposed at the
22 output of each logic tree. In such an arrangement, array transmission losses are
23 minimized and one source of electromagnetic energy per logic tree is required.

24

25 In another arrangement using a similar imaging array, transmission losses are reduced
26 over prior art input arrangements and the number of sources of electromagnetic energy is
27 reduced to one. Using an input logic tree, fed from a single laser and arranged
28 perpendicularly to the array logic trees, the imaging cells of the input logic tree act as
29 inputs to the stacked logic trees of the imaging array. In this way, a scanned line is

1 delivered from each imaging cell of the input logic tree to the first active element of an
2 associated array logic tree. From there, under control of a programmable pulsed
3 generator, portions of the scanned line are directed to the output imaging cells of each of
4 the array logic trees. Two-dimensional images are built up in this way by activating the
5 imaging cells of each array logic tree in succession. Three dimensional images may be
6 obtained using an approach similar to that just described by interleaving stereo displaced
7 images from a 3-D camera at the output imaging cells of an imaging array by activating
8 the first and every other logic tree of an array with one image and the second and every
9 other logic tree with a stereo displaced image. Glasses which respond to a different
10 polarization for each eye are required to produce the 3-D effect.

11
12 The present invention also relates to a method of fabricating the above described imaging
13 arrays by slicing stacked alternating layers of insulating material and CLC material. To
14 the extent that a final imaging array requires that each logic tree have twice as many CLC
15 elements per stage as a preceding stage after the first stage, the number of CLC element
16 per stage may doubled by halving the spacing between CLC elements during fabrication.
17 This is accomplished by halving the thickness of the insulation disposed between layers
18 of CLC material after setting the initial spacing between CLC layers which are to be used
19 as the first stage of a logic tree. After producing layers of a given thickness by slicing at
20 a 45° angle, transparent metallic ground planes are formed. Then, using photolithographic
21 and etching techniques, electrodes are formed over every other CLC element. A spacer
22 element is then fixed to the periphery of each layer and the resulting volume is filled with
23 a phase shifter material. The resulting stages are then stacked using as many as required
24 to form an imaging array with a desired number of imaging cells. Stacking the stages,
25 which are slices containing differently spaced CLC elements automatically provides the
26 logic trees which deliver scanned line to the output imaging cells. The method uses
27 mass-production techniques and results in an inexpensive, flat-panel display.

DESCRIPTION OF THE PRIOR ART

Generally, there are two well-known techniques for the steering and scanning of light beams. One is electromechanical and the other is acousto-optical. Both techniques have severe limitations. One such limitation is that arrangements incorporating these techniques require a large volume due to the small angle through which the light beam can be deflected. Thus, if it is desired to scan a length B, the deflection arrangement has to be positioned a distance, A, providing an A/B ratio larger than 1.

All known systems require an A/B ratio larger than 1 and to the extent that the arrangement of the present application can provide A/B ratios which are very much less than 1, the resulting structure may also be characterized as a flat-panel display. In the known scanning approaches, scanning speed is relatively sluggish due to the use of electromechanical or electro-acoustic elements. Because such devices are eliminated in the scanning arrangement of the present application, scanning speeds in the microsecond range are achievable.

U.S. Patent 4,670,744 filed March 14, 1985 and issued June 2, 1987 in the name of T. Buzak incorporates variable optical retarders and liquid crystal chiral cells. This reference takes advantage of the reflective and transmissive characteristics of chiral cells as well as the ability of variable optical retarders to convert one circular polarization to the other circular polarization. However, when a beam containing image information is projected along a given path in which the chiral cells and retarders are disposed, the beam remains in that given path or is retroreflected along the same path. Opposed to this, the arrangements of the present application while they all incorporate the reflection-transmission characteristics of chiral cells, they all incorporate an ability to divert the reflected beams into other paths. To the extent that the Buzak reference seek to provide a three-dimensional display, all the images reflected must lie in a plane parallel to the planes of the chiral cells. Otherwise distortion and degradation of the reflected images would occur due to the required lateral displacement of the chiral cells. In other words, to

1 provide the desired result, no diversion of the beam in the Buzak reference can be
2 tolerated.

3
4 U.S. Patent 5,221,982, filed July 5, 1991 and issued on June 22, 1993 to S. M. Faris is
5 entitled Polarizing Wavelength Separator. The patent relates to a polarizing wavelength
6 separating optical element in the form of a flat panel which causes each of a plurality of
7 polychromatic optical beams from a source, entering at one surface and transmitted to
8 another surface, to be converted, with high conversion efficiency, into circularly
9 polarized, spectrally and spatially separated beams. The element is made of a periodic
10 array of cells; each of the latter incorporating a plurality of subcells. One subcell
11 functions as a broadband reflector, while each of the remaining subcells acts as a
12 polarizing, wavelength selective reflector. Each subcell comprises a plurality of layers
13 which are bonded together at their surfaces and are oriented at a 45° angle relative to the
14 horizontal surfaces of the panel. In each subcell, the plurality of layers comprise two
15 cholesteric liquid crystal, CLC films, which reflect at a selected wavelength, at least one
16 optical retarder and clear substrates which provide mechanical support. The thicknesses
17 of the supporting substrates are designed to cause the beams transmitted through the
18 element to be spatially separated by appropriate distances.

19
20 In the reference, all the elements utilized in the panel are passive in character which
21 constrain beams of electromagnetic energy into paths which are fixed for all time. In
22 contradistinction to this, the present application, with its electronically controllable
23 retarders, provides paths for electromagnetic energy which can be changed from instant-
24 to-instant taking advantage of both the transmissive and reflective capabilities of CLC
25 elements. The combination of a circularly polarized input with controllable retarders and
26 associated CLC elements in the present invention provides the ability to scan a beam
27 from point to point in a panel-like display or to steer a beam it can emanate from any
28 location on an array of imaging cells. Strictly passive arrays with their fixed paths cannot
29 achieve these results.

1
2 U.S. Patent 5,459,591, filed March 9, 1994 and issued October 17, 1995 to S. M. Faris
3 relates to beam steering and scanning devices which utilize an imaging cell which
4 incorporates a solid-state cholesteric liquid crystal (CLC) element, an electronically
5 controlled, variable half-wave retarder and a source of circularly polarized light. The
6 CLC element is disposed to an angle (45°) relative to the path along which light from the
7 source is projected and is designed to reflect, at a given wavelength, one circular
8 polarization of light and transmit the other. Using this characteristic, light of one
9 polarization or the other is presented to the variable retarder and depending on whether or
10 not it is actuated, light is either diverted into another orthogonal path or remains in the
11 original path. If another similar imaging cell is disposed in the orthogonal path, light
12 incident on that cell can also be diverted into yet another path or transmitted along the
13 orthogonal path under control of a half-wave retarders associated with said another
14 imaging cells. By arranging a plurality of imaging cells in the form of an array and
15 accessing each row of the cells of the array with a column of similar imaging cells and by
16 selectively activating half-wave retarders associated with each of the cells,
17 monochromatic or polychromatic light from a single source or multiple sources may be
18 steered to a selected cell and reflected from its associated CLC element or elements.
19 Utilizing successive cells in the array and causing reflection of a modulated beam or
20 beams provides a frame in the manner of the usual TV set which is viewed by the eyes as
21 an integrated picture. Successive frames, of course, provide the usual moving images.
22

23 SUMMARY OF THE INVENTION 24

25 The present invention relates to beam steering and scanning devices which utilize
26 cholesteric liquid crystal (CLC) elements arranged in branches to form a logic tree. Each
27 branch comprises an active and passive CLC element; the former further comprising a
28 half-wave retarder and an electrode and the latter only the CLC element. Each
29 succeeding branch contains twice as many branches as a preceding branch and, by
30 activating active CLC element electrodes under control of a programmable pulsed source,

1 inputs applied to the first stage of a logic tree are delivered as a scanned line of
2 electromagnetic energy or light to the imaging cells of the last stage of the logic tree. By
3 stacking identical logic trees with a laser source for each tree, a flat panel imaging array
4 or display device is formed in which the transmission losses are minimized.

5
6 Using a similar imaging array, transmission losses may be further reduced by using a
7 logic tree the outputs of which act as inputs to the imaging array where formerly a
8 plurality of lasers were required. By positioning an input logic tree perpendicularly to the
9 similar logic trees of the imaging array, a single source of energy provides an output at
10 each of its imaging cells which acts as an input to an associated logic tree of the imaging
11 array. 2-D and 3-D images are provided by applying modulation to lasers from standard
12 T.V. cameras and cameras designed to provide stereo displaced images respectively. In
13 the array which provides 3-D images, an image and a stereo displaced image are
14 interleaved to provide the desired images each of which has a different circular
15 polarization.

16
17 The present invention also relates to a method of fabricating structures which provide the
18 above described features. Since all the stages of a logic tree differ only in the number of
19 branches they contain, it was recognized that light beams, for example, applied from a
20 laser beam could pass through a number of stages with minimum dispersion and maintain
21 its original position even though relatively large structures are used to control its position.
22 This recognition permitted the use of CLC elements, electrodes and half-wave retarder
23 material which need not be divided into discrete elements in each logic tree. Thus, each
24 CLC element, each electrode and each retarder material may extend from top-to-bottom
25 or from side-to-side in each stage of an imaging array.

26
27 Stages are fabricated by slicing layers of insulating material and CLC material at an angle
28 of 45° . The thickness of the insulating material controls the spacing between the
29 resulting CLC elements. Transparent layers, such as indium tin oxide are formed on both
30 sides of the layer or layers containing spaced CLC elements. Using photolithographic

1 techniques, one side is masked and etched to form an electrode over every other CLC
2 element. A spacer element fixed to the periphery of each layer where the electrodes have
3 been etched forms a volume into which half-wave retarder material is introduced in liquid
4 form. In this way, stages containing two, four, eight, sixteen, CLC elements and so on
5 have been mass produced. The stages are then stacked so that each stage contains
6 twice as many CLC elements as a preceding stage forming logic trees the imaging cells of
7 which form an array.

8
9 The above described arrangements and their fabrication technique provide flat panel
10 displays which substantially reduce transmission losses and the number of energizing
11 sources. These features combined with a novel and inexpensive manufacturing technique
12 are able to deliver a flat panel display which requires neither a vacuum envelope nor
13 unacceptable high voltages.

14
15 It is, therefore, an object of the present invention to provide an imaging array which has
16 reduced transmission losses compared to prior art arrays.

17
18 Another object is to provide an imaging array which reduces transmission losses while
19 simultaneously reducing the electromagnetic source requirement to one source.

20
21 Still another object is to provide an improved flat panel display which is capable of
22 providing both 2-D and 3-D images.

23
24 Still another object is to provide a method of fabricating flat panel display which is
25 inexpensive and conducive to mass-production techniques.

26
27 The foregoing objects and features of the present invention will become apparent from
28 the following more detailed description of preferred embodiments taken in conjunction
29 with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a schematic drawing of a logic tree of active and passive Cholesteric Liquid Crystal (CLC) elements so arranged that a single input to a first stage of the logic tree may be delivered to any one of the outputs of the last stage of the logic tree.

FIG. 2 is a schematic diagram of a logic tree similar to that shown in FIG. 1 which shows that the polarization of CLC members may be varied to produce outputs having polarizations different from those shown in FIG. 1.

FIG. 3 is an orthographic projection of eight logic trees positioned one atop the other which, in accordance with the present invention, provide 64 outputs using one source of electromagnetic radiation per logic tree.

FIG. 4 is an orthographic projection similar to FIG. 3 except that, instead of a plurality of sources of electromagnetic radiation, only a single source of radiation in combination with a logic tree like that shown in FIG. 1 and disposed perpendicularly to the stacked logic trees of FIG. 3 is required.

FIG 5 is an orthographic projection of an imaging array and its associated electronics which, in conjunction with viewing glasses and stereo displaced images, provides a 3-D display.

FIG. 6 is an orthographic, cut-away projection of a plurality of layers of insulating material like SiO_2 and a plurality of layers of CLC material interleaved with the layers of insulating material. The interleaved layers are sliced at an angle of 45° .

FIG. 7 is a cross-sectional view of a layer of insulating material in which CLC members are disposed at an angle of 45° and are spaced apart by a distance t .

1
2 FIG. 8 is a cross-sectional view of a layer of insulating material in which CLC members
3 are disposed at an angle of 45° and is similar to FIG. 7 except that the CLC members are
4 spaced apart by a distance $t/2$.

5
6 FIG. 9 is a cross-sectional view similar to that shown in FIG. 7 except that the CLC
7 members are spaced apart by a distance $t/4$.

8
9 FIG. 10 is a cross-sectional, orthographic projection of an insulation layer with CLC
10 members disposed at an angle of 45° therein like that shown in FIG. 8 and further
11 includes a ground plane disposed on the bottom thereof.

12
13 FIG. 11 is a cross-sectional orthographic projection similar to FIG. 10 except that it
14 further includes electrodes disposed over every other CLC member.

15
16 FIG. 12 is a cross-sectional view similar to that shown in FIG. 11 further including a
17 spacer disposed around the periphery of the layer of insulation material with the resulting
18 enclosed volume filled with a phase-shifter material in liquid form.

19
20 FIG. 13 is a top view of a logic tree made up of layers like those shown in Figs. 7-12 after
21 they have been stacked and aligned to form an array like those shown in FIGS. 4, 5.

22 23 DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

24
25 Referring now to FIG. 1, there is shown a schematic drawing of a logic tree of active and
26 passive Cholesteric Liquid Crystal (CLC) elements which are so arranged and controlled
27 that a single input to a first stage of the logic tree may be delivered to any one of the
28 outputs of the last stage of the logic tree by appropriately switching electronically
29 controlled half-wave retarders associated with the active CLC elements of the logic tree.
30 By programming the switching of the half-wave retarders of each stage of the logic tree, a

1 laser input to the first stage of the logic tree may, for example, provide a scanned version
2 of the input at the outputs of the last stage of the logic tree. The application of the logic
3 tree as a scanner will be described in detail in what follows. It will also become clear that
4 the same embodiment has other applications.

5
6 Considering FIG. 1 in more detail, logic tree 1 is shown consisting of a plurality of stages
7 labeled STAGE 1-STAGE 4 wherein each stage includes one or more branches each of
8 which consists of an active and passive CLC element. Thus, STAGE 1 consists of a
9 branch 2 which, in turn, includes active CLC element 18 and passive CLC element 19.
10 STAGE 2 consists of branches 3, 4; the former including active CLC element 21 and
11 passive CLC element 22 while the latter includes active CLC element 23 and passive
12 CLC element 24. STAGE 3 consists of four branches 5-8 each of the branches consisting
13 of active and passive CLC elements 31, 33, 35, 37 and 32, 34, 36, 38, respectively.
14 Similarly, STAGE 4 consists of eight branches 9-16 each of these branches including
15 active and passive CLC elements 41, 43, 45, 47, 49, 51, 53, 55 and 42, 44, 46, 48, 50, 52,
16 54, and 56, respectively, just like the previously mentioned branches. At this point, it
17 should be appreciated that many more stages may be added to tree 1 with each succeeding
18 stages having twice as many branches as the preceding stage. Using this approach,
19 STAGE 4 in FIG. 1 has 2^{n-1} branches wherein n is the stage number. Thus, STAGE 4 has
20 2^{4-1} or eight branches. Since each branch has two CLC elements, each stage has 2^n
21 elements and, for STAGE 4, sixteen elements. Thus, STAGE 10, for example, would
22 have 2^{10} or 1024 CLC elements providing one light output per element or 1024 outputs.

23
24 Since FIG. 1 is representative of the way logic tree 1 operates regardless of the number of
25 stages, only four stages have been incorporated to clearly demonstrate how such a logic
26 tree may be used to provide a scanned light output from a plurality of elements which are
27 activated by an input from a single source of electromagnetic energy.

28
29 Before describing the operation of FIG. 1, it should be understood that the active CLC
30 elements of each branch in FIG. 1 do not depart from similar active elements shown in

FIG. 1 of U.S. Patent 5,459,591 entitled "Electromagnetic Energy Beam Steering Devices" in the name of S.M. Faris, which is hereby incorporated by reference. The passive CLC elements of the present invention differ from the active CLC elements in that the passive CLC elements do not incorporate an electronically controlled, variable half-wave retarder or π -cell. Thus, each branch of logic tree 1 as represented by branch 2 of FIG. 1 includes an active CLC element 18 and a passive CLC element 19. The former includes a cholesteric liquid crystal member 60, a transparent electrode 62, a ground plane (not shown), and a controllable half-wave retarder 61 while the latter includes a cholesteric liquid crystal member which is identical to member 60. Since each of the branches 3, 4, 5-8 and 9-16 is identical with branch 2 of FIG. 1, each cholesteric liquid crystal element and each half-wave retarder of each branch is identified with the same reference numbers 60, 61 respectively.

In FIG. 1, active CLC element 18 and passive CLC element 19 of branch 2 both include cholesteric liquid crystal members 60 which are disposed at an angle, preferably 45° , within each of the elements 18, 19. Members 60 are made from a nematic liquid crystal material with chiral additives or polysiloxane side-chain polymers which cause the cigar-shaped molecules to be spontaneously aligned in an optically active structure of either a left-handed or right-handed helix with a helical pitch, P. The twisting direction and the pitch, P, of the helices are determined by the nature and concentration of the additives. A CLC member, like member 60, has all its helices aligned in one direction and is capable of reflecting light, for example, having one circular polarization having a characteristic wavelength or band of wavelengths. Cholesteric Liquid Crystal (CLC) members 60 which are used in the practice of the present invention and their method of fabrication are shown in U.S. Patent 5,221,982, filed July 5, 1991 and issued on June 22, 1993 in the name of S.M. Faris. This patent is herewith incorporated by reference. While CLC members 60 are shown in FIG. 1 as being single elements, it should be understood that a plurality of CLC members 60 may be substituted for each of the members 60 to provide for the reflection and transmission of circularly polarized radiation having a plurality of wavelengths or band of wavelengths which are provided by a plurality of

1 sources of electromagnetic radiation. It should be appreciated that, in the practice of the
2 present invention, members 60 may be made of any material which can be switched to
3 reflect and/or transmit electromagnetic energy by the application of electric or magnetic
4 fields to that material.

5
6 Half-wave retarders or π -cells 61 shown schematically in FIG. 1 are of the type shown
7 and described in U.S. Patent, 4,670,744, filed March 14, 1985 and issued on June 2, 1987
8 in the name of T.S. Buzak and may be utilized in the practice of the present invention.
9 The Buzak patent is herewith incorporated by reference. Alternatively, instead of CLC
10 films, polarizing reflectors, polarizing prisms or McNeill prisms may be utilized in the
11 practice of the present invention and are commercially available. When more than a
12 single wavelength of electromagnetic radiation is used in the arrangement of FIG. 1, a
13 broad band π -cell may be utilized to provide half-wave retardation of each wavelength to
14 maintain the same intensity level for each wavelength.

15
16 Logic tree 1 of FIG. 1 is activated from a source 17 of electromagnetic radiation which
17 may be a laser or any other source of radiation the output of which may be converted
18 from a linearly polarized orientation to a circularly polarized orientation by means of a
19 quarter-wave plate (not shown) in a manner well known to those skilled in the optical
20 arts. If the resulting output is not appropriately polarized, a half-wave retarder may be
21 utilized to provide the conversion from one circular polarization to the other polarization.

22
23 For purposes of the present application, radiation emanating from source 17 is circularly
24 polarized in either a clockwise or counter-clockwise direction. Lasers which are
25 commercially available may be utilized to provide outputs which fall within the visible,
26 infrared or ultraviolet spectra. While source 17 is shown as a single source in FIG. 1, it
27 should be appreciated that it also represents a plurality of sources each having a different
28 wavelength. Thus, source 17 may include lasers which emit at the red, green and blue
29 wavelengths of the visible spectrum so that the projected beam of radiation is a beam of
30 light having a single color or combinations of these wavelengths.

1
2 It should also be appreciated that source 17 may comprise lasers or other sources of
3 electromagnetic radiation which are capable of being intensity modulated. In this way,
4 the source output may be varied in intensity from zero to a maximum intensity including
5 all gradations in between.

6
7 In FIG.1, source of electromagnetic radiation 17 is shown directly irradiating a member
8 60 of active element 18 of branch 2 from which it is either transmitted or reflected
9 depending on the polarization of the emitted radiation. The emitted radiation from source
10 17 may have a single intensity or it may be an intensity modulated signal provided by a
11 television camera 25 or the like. By appropriately programming π -cells or half-wave
12 retarders 61, an unmodulated or intensity modulated signal is delivered in a scanned
13 manner to the active and passive CLC elements 41-56 of branches 9-16 of STAGE 4. In
14 this way, an unmodulated or intensity modulated beam of radiation is scanned across
15 elements 41-56 providing an output which is similar in every way to a single scan line of
16 a conventional television set.

17
18 If an input is provided in digital form, a digital-to-analog converter 26 may be interposed
19 between camera 25 and source 17 in a well-known manner.

20
21 In FIG.1, variable half-wave retarders 61 are activated by a programmable pulsed source
22 27 which gets timing information from camera 25 via interconnection 28. A plurality of
23 driver interconnections 29 extend from pulsed source 27 and each interconnection 29 is
24 connected to a separate electrode 62 which applies an electric field to an associated half-
25 wave retarder 61 when activated by pulsed source 27. In FIG. 1, fifteen driver
26 interconnections 29 would be utilized each one of which, when pulsed, activates a
27 separate variable half-wave retarder 61.

28
29 In operation, logic tree 1 is activated when source 17 is activated. The object is to
30 provide a scanned output from a single input to a plurality of outputs in STAGE 4 of

1 logic tree 1. It is, therefore, required that the outputs of active and passive elements 41,
2 43, 45, 47, 49, 51, 53, 55 and 42, 44, 46, 48, 50, 52, 54 and 56, respectively, be activated
3 so that outputs are obtained from these elements in the order shown in FIG.1. Since
4 element 41 is to provide the first output, if the input signal is right-hand circularly
5 polarized (RCP) radiation and all members 60 are designed to be reflective of left-hand
6 circularly polarized (LCP) radiation, the RCP light passes through active elements 18, 21,
7 31 and 41 unhindered since these elements reflect LCP radiation and transmit RCP
8 radiation. An RCP radiation output, therefore, appears at the output port of element 41.

9
10 In the next time period, half-wave retarder 61 of element 41 is activated by a pulse from
11 pulsed source 27 via an interconnection 29 to electrode 62 causing retarder 61 to
12 introduce a half-wave delay into the input RCP radiation which has passed through active
13 elements 18, 21 and 31 causing the RCP radiation to be converted to LCP radiation. The
14 LCP radiation then reflects from member 60 of element 41 which is reflective of LCP
15 radiation toward member 60 of element 42 which is also reflective of LCP radiation. The
16 impinging LCP radiation is then reflected to the output port of element 42.

17
18 In the next time period, an output is desired from the output port of active element 43. To
19 accomplish this, retarders 61 at the inputs of active elements 31 of STAGE 3 and active
20 elements 43 of STAGE 4 are activated by applying pulses to their associated transparent
21 electrodes 62.

22
23 Once this is done, the RCP radiation at the input of active element 31 is converted to LCP
24 radiation and reflects from LCP reflective member 60 over to LCP reflective member 60
25 of passive element 32 where it is reflected toward active element 43. The LCP input at
26 active element 43 encounters a half-wave retarder 61 and is converted to RCP radiation.
27 The latter then passes unaffected to the output port of active element 43 because its CLC
28 member 60 reflects only LCP radiation.

1 In the next interval, pulsed source 27 deactivates half-wave retarder 61 associated with
2 active element 43 and continues activation of the half-wave retarder 61 associated with
3 active element 31. In this way, the LCP radiation impinging on element 43 encounters no
4 delay and remains as LCP radiation which is then reflected from LCP reflective member
5 60 of element 43 toward passive element 44. The thus reflected LCP radiation is
6 reflected from LCP reflective member 60 of element 44 to its output port.

7
8 Rather than tediously describing every passage through every element, the order of the
9 activation of half-wave retarders 61 will be described since every path from input to
10 output port can be gleaned from the previous description and drawing shown in FIG. 1.
11 To obtain an output at active element 45, only the variable half-wave retarders 61
12 associated with active elements 21 and 33 must be activated. To obtain an output at
13 active element 46, variable half-wave retarders 61 associated with active elements 21, 33
14 and 45 must be activated. To obtain an output at active element 47, the variable half-
15 wave retarders associated with active elements 21 and 47 must be activated. To obtain an
16 output at passive element 48, only the variable half-wave retarder associated with active
17 element 21 need be activated. An output at active element 49 may be obtained by
18 activating the half-wave retarders associated with active elements 18 and 23. An output
19 at passive element 50 may be obtained by activating the half-wave retarders associated
20 with active elements 18, 23 and 49. To obtain an output at active element 51, the half-
21 wave retarders associated with active elements 18, 35 and 51 must be activated. An
22 output may be obtained from passive element 52 by activating half-wave retarders 61
23 associated with active elements 18, 23 and 35. To obtain an output at active element 53,
24 half-wave retarders 61 associated with active elements 18 and 37 must be activated. An
25 output at passive element 54 may be obtained by activating half-wave retarders 61
26 associated with active elements 18, 37 and 53. To obtain an output at active element 55,
27 half-wave retarders 61 associated with active elements 18 and 55 are activated. Finally,
28 active element 56 is activated by activating half-wave retarder 61 associated with active
29 element 18.

30

1 Once half-wave retarders 61 are activated by applying pulses to transparent electrodes 62
2 from programmable pulsed source 27 as described hereinabove, a scanned output varying
3 in intensity at each of the active and passive elements 41 through 56 is obtained. The
4 outputs do not all have the same polarization and, for the embodiment of FIG. 1, have a
5 polarization pattern of alternating RCP and LCP as the elements are scanned from left to
6 right. Recognizing that such variation is present is important where outputs having the
7 same circular polarization are desired or required so that fixed half-wave retarders may be
8 placed to convert all the polarization's to the same polarization. Thus, in FIG. 1, for
9 example, fixed half-wave retarders 63 may be placed at the outputs of active elements 41,
10 43, 45, 47, 49, 51, 53 and 55 to convert their RCP outputs to LCP. The ability to do this
11 conversion is particularly important in arrangements which provide a 3-D output because
12 the perception of 3-D is based on having two spatially displaced images each of which
13 has a different polarization.

14
15 If the input to active CLC element 18 in FIG. 1 is changed to LCP and all the CLC
16 members 60 in logic tree 1 are changed to be reflective of RCP, the outputs obtained are
17 exactly the same as those shown in FIG.1.

18
19 An identical output pattern to that shown in FIG. 1 is obtainable where the input is LCP
20 and all the members 60 are reflective of LCP.

21
22 A pattern opposite to that shown in FIG. 1 is obtainable where the input is RCP and all
23 the members 60 are reflective of RCP.

24
25 FIG. 2 is a schematic diagram of a logic tree 1 similar to that shown in FIG. 1. It shows
26 only the logic tree without the associated laser and electronics. The purpose is to show
27 that the polarization of members 60 reflective of different polarizations may be varied to
28 produce outputs having different polarizations from those shown in FIG. 1. Each of the
29 boxes representing active and passive elements in FIG. 2 contains either the letter L or R
30 indicating that the CLC member 60 therein is reflective of either left-handed or right-

1 handed circular polarization. Without going into exhaustive detail, suffice it to say that
2 the outputs shown in FIG. 2 are obtained from an LCP input having the following
3 polarization pattern when retarders 61 are switched in the same order as described in
4 connection with FIG. 1:

5
6 **LRRL RLLR RLLR LRRL**

7
8 A pattern different from that shown above would be obtained if the input polarization
9 were changed to RCP and members 60 of logic tree 1 were reflective of polarization's
10 opposite to those shown in FIG. 2. The output pattern is as follows:

11
12 **RLLR LRRL LRRL RLLR**

13
14
15 The foregoing illustrates how the output polarization may be controlled for applications
16 where information is polarization encoded or scrambled; transmitted and decoded or
17 unscrambled by using a key which controls the variable half-wave retarders 61.

18
19 From the point of view of ease of manufacturing, logic trees having the same CLC
20 members 60 are the most advantageous as will be seen when the fabrication process is
21 described hereinbelow.

22
23 The arrangement of FIG. 1 provides an advantage over the scanning arrangement shown
24 in U.S. Patent 5,459,591 in that input light has to traverse, in a 1024 x 1024 array, 1024
25 CLC members 2 (in the patent) to provide an output at its furthest imaging cell 1 (in the
26 patent). If each CLC member has transmissibility (T), the final imaging cell will have a
27 transmissibility of $(T)^{1024}$. Thus, even with a transmissibility approaching 1, say .999,
28 the output at the 1024TH imaging cell would be: $(.999)^{1024}$ which, to all intents and
29 purposes, is zero.

1 Opposed to this is the present approach where, to provide the last output in a 1024 x 1024
2 array, only twenty CLC members 60 or two per stage need to be traversed providing a
3 transmissibility of $(T)^{20}$. Under these conditions the 1024th output, assuming $T = .999$,
4 would be $(.999)^{20}$ which is approximately ninety percent of the input intensity. The
5 minimum transmissibility for a ten stage array would be $(T)^{10}$ or one transition per stage.

6
7 From the foregoing, while logic tree 1 of FIG. 1 represents an improvement over the prior
8 art in terms of output light intensity, it should be clear that each logic tree 1 requires its
9 own input laser or source of electromagnetic radiation 17. Thus, to provide an 8 x 8
10 array, for example, eight logic trees 1 would have to be stacked in the manner shown in
11 FIG. 3.

12
13 FIG. 3 is an orthographic projection of eight logic trees 1 positioned one atop the other
14 which, in accordance with the teaching of the present application, provide 64 outputs.
15 One source of electromagnetic radiation 17 per logic tree 1 is required.

16
17 Because of space limitations, the showing of FIG. 3 has been limited to the use of only
18 three of the STAGES of FIG. 1. Also, since each of logic trees 1 in FIG. 3 is identical
19 with the other logic trees 1, only the topmost logic tree 1 with its CLC members 60 and
20 variable half-wave retarders 61 have been shown. Also, as will become clear hereinafter,
21 the dimensions shown are not to scale.

22
23 In FIG. 3, 8x8 array 70 is shown which comprises eight logic trees 1 stacked one atop the
24 other. Each logic tree 1 is comprised of three stages, STAGE 1, STAGE 2, and STAGE
25 3. STAGE 1 comprises branch 2; STAGE 2 comprises branches 3,4 and STAGE 3
26 comprises branches 5-8 as shown in FIG. 1. Each branch includes active and passive
27 CLC elements similar to those shown in STAGES 1-3 of FIG. 1 and each of the active
28 and passive elements includes a cholesteric liquid crystal member 60 which is positioned
29 at an angle of 45° within each of the active and passive elements of array 70. Also,
30 included are variable half-wave retarders 61 which are arranged in FIG. 3 just like the

1 variable retarders 61 in STAGES 1-3 of FIG. 1. In FIG. 3, each logic tree 1 is activated
2 by an associated source of electromagnetic radiation 17, preferably a laser, thus requiring
3 a total of eight sources 17. As each laser is actuated, variable half-wave retarders 61 are
4 actuated as described in connection with FIG. 1 hereinabove and the output of each laser
5 17 appears as a scanned modulated signal going from left to right at the outputs of
6 imaging cells 71 of each of logic trees 1. In the arrangement shown in FIG. 3, sources 17
7 and retarders 61 may be actuated sequentially or simultaneously. If the outputs of
8 sources 17 are converted to right-hand circular polarization (RCP) and all CLC members
9 60 are reflective of left-hand circular polarization (LCP), the outputs of each logic tree 1
10 of FIG. 3 will be the same as those shown in FIG. 1, namely:

11
12 **RLRL RLRL**
13

14 As suggested in connection with the description of FIG. 1, fixed half-wave retarders may
15 be appropriately positioned to make all the outputs have the same polarization.
16

17 While the number of lossy transitions per logic tree has been reduced over that shown in
18 the prior art, this has been accomplished by the use of a source 17 for each logic tree 1
19 incorporated in an array 70. With arrangements like that shown in FIG. 3 expanded to a
20 1024 x 1024 array, for example, 1024 sources 17 would be required. This requirement
21 can be eliminated and the number of sources reduced to one by using a logic tree 1 like
22 that shown in FIG. 1, the outputs of which, provided from a single source 17, act as
23 inputs to an array 70 like that shown in FIG. 3.
24

25 This will become clear from a consideration of FIG. 4 which is an orthographic
26 projection similar to FIG. 3 except that, instead of a plurality of sources 17, only a single
27 source 17, in combination with a logic tree 1 like that shown in FIG. 1, disposed
28 perpendicularly to the logic trees 1 of FIG. 3 is required.
29

1 Considering FIG. 4 in more detail, array 70 is identical with array 70 shown in FIG. 3.
2 Also, source of electromagnetic radiation 17 in FIG. 4 is similar to sources 17 shown in
3 FIG. 3. In FIG. 4, an input logic tree 72 is shown disposed between array 70 and source
4 17 such that each imaging cell 71 of logic tree 72 acts as an input to an associated logic
5 tree 1 of array 70. Thus, the uppermost imaging cell 71 of input logic tree 72 provides an
6 input to the leftmost element of the topmost of logic trees 1 of array 70. This input which
7 may be an intensity modulated signal from source 17, is scanned across the imaging cells
8 71 of the topmost logic tree 1 of array 70 in a manner analogous to the scan of a
9 television frame. When the scanned output of the topmost logic tree 1 reaches its last
10 imaging cell 71, the output of source 17 is switched to the next imaging cell 71
11 (immediately beneath the topmost imaging cell 71) of logic tree 72. The output of that
12 next imaging cell then acts as the input to the logic tree 1 immediately beneath the
13 topmost logic tree 1 of array 70. The inputs to the last mentioned logic tree 1 are then
14 delivered to the imaging cells 71 of that logic tree 1 in sequence from left-to-right
15 providing a scanned, intensity modulated signal similar to that of a television scan line.

16
17 Each of the remaining imaging cells 71 of input logic tree 72 is then actuated by
18 programming electrodes 62 and variable half-wave retarders 61 associated with logic tree
19 72 in the same manner described hereinabove in connection with FIG. 1. Similarly, each
20 of the logic trees 1 of array 70 is actuated by outputs from an associated imaging cell 71
21 of input logic tree 72. Then, under control of programmed electrodes 62 and half-wave
22 retarders 61, these outputs, now inputs, to an associated logic tree 1, are delivered to the
23 imaging cells 71 of each logic tree 1 as a scanned line having portions which may vary in
24 intensity from imaging cell 71-to-imaging cell 71. In this way, by accessing logic trees 1
25 from top-to-bottom, for example, in FIG. 4, an image is built up which, depending on the
26 imaging cell density, can provide images of extremely high resolution.

27
28 From the foregoing, it should be clear that the modulated output of a single source 17,
29 preferably a laser, may be delivered to the imaging cells 71 of a plurality of stacked logic
30 trees 1 like array 70 in FIG. 4. As shown in FIG. 4, the use of an input logic tree 72

1 permits the use of a single source 17 as opposed to the multiplicity of sources 17 shown
2 in FIG. 3. The value of the arrangement shown in FIG. 4 becomes more apparent when it
3 is recalled that for a 1024 x 1024 array embodiment like FIG. 3, 1024 lasers would be
4 required. Thus, in addition to reducing the number of lossy transitions as provided by the
5 embodiment of FIG. 3, the embodiment shown in FIG. 4 also reduces the number of
6 sources 17 required to the absolute minimum of one. While the electronic equipment
7 required to operate displays like those shown in FIGS. 3, 4, has not been shown, it should
8 be appreciated that the same components as shown in FIG. 1 and which are well-known
9 in the imaging arts may be utilized in the practice of the present invention. Thus, timing
10 information obtained from camera 25, for example, is applied via interconnection 28 to
11 programmable pulsed sources 27. The latter then applies switching signals to both logic
12 tree 72 and each of logic trees 1 to appropriately control their electrodes 62 and half-wave
13 retarders 60 so that a scanned energy output may be delivered from the imaging cells 71
14 of each logic tree 1 and input logic tree 72.

15
16 Referring now to FIG. 5, an orthographic projection of an imaging array is shown which,
17 in combination with viewing glasses and stereo displaced images provides a 3-D display.

18
19 In FIG. 5, input logic tree 72 is accessed by a source 17 of electromagnetic radiation
20 which is modulated by outputs of a stereoscopic television camera 73 via interconnection
21 74. The two outputs from stereo camera 73 are stereo displaced so that, if they are
22 separated one from the other by some characteristic like polarization, the two resulting
23 images may be delivered one to each eye (using appropriate glasses) and combined in the
24 brain to provide a three-dimensional image.

25
26 One of the images is provided by applying scanned lines from stereo camera 73 via
27 interconnection 74 to laser 17. The output of the latter is then applied to input logic tree
28 72 from which scanned line outputs are delivered from the topmost and alternate imaging
29 cells 71 under control of programmable pulsed source 75 which actuates variable half-
30 wave retarders 61 thereof via interconnections 76. The output from the topmost of

1 imaging cells 71 of input logic tree 72 is applied for a given interval to leftmost member
2 60 of the uppermost of logic trees 1. At the same time, variable half-wave retarders 61
3 under control of programmable pulsed source 27 are appropriately actuated so that a
4 portion of the scanned line from stereo camera 73 is delivered to each of the imaging cells
5 71 of the uppermost of logic trees 1 of array 70.

6
7 In the instance of FIG. 5, each imaging cell 71 of array 70 is illuminated for a time equal
8 to 1/8 the given interval of a scanned line from camera 73. For a 1024 x 1024 array, the
9 illuminating time would be $1/1024^{\text{TH}}$ of the scanned line interval.

10
11 The first image is completed by applying scanned lines from stereo camera 73 via
12 interconnection 74 which modulate laser 17 during each alternate interval after the first to
13 each alternate imaging cell 71 after the first imaging cell 71 of input logic tree 72. Each
14 scanned line is delivered to the imaging cells 71 of each alternate logic tree 1 of array 70
15 in the same manner described in connection with the delivery of the first scanned line to
16 the uppermost of logic trees 1 of array 70.

17
18 The stereo displaced image from stereo camera 73 is delivered as scanned lines via
19 interconnection 74 to laser 17 where they modulate the output of laser 17. The stereo
20 displaced scanned line outputs are delivered to laser 17 during the second and alternate
21 intervals after the second interval. The first stereo displaced output from laser 17, under
22 control of programmable pulsed source 75 which appropriately actuates the variable half-
23 wave retarders 61 of input logic tree 72, is delivered to the second-from-the-top of
24 imaging cells 71 of logic tree 72 as a scanned line. This last mentioned output acting as
25 an input to the leftmost CLC member 60 of the second-from-the-top of logic trees 1 of
26 array 70 is delivered to the imaging cells 71 of the second-from-the-top of logic trees 1 of
27 array 70 under control of programmable pulsed source 27 as portions of the scanned line
28 output of laser 17.

1 As with the first image generation, the imaging cells 71 of the stereo displaced image are
2 illuminated for a time equal to $1/8$ the given interval of a scanned line.

3
4 The stereo displaced image is completed by applying scanned lines from stereo camera
5 73 via interconnection 74 to laser 17 during each alternate interval after the second
6 interval to each alternate imaging cell 71 after the second imaging cell 71 of input logic
7 tree 72. Each stereo displaced scanned line is delivered to the imaging cells 71 of the
8 second and alternate logic trees 1 of array 70 in the same manner described in connection
9 with the delivery of the first stereo displaced scanned line to the second-from-the-top of
10 logic trees 1 of array 70.

11
12 If the polarization applied to logic trees 1 is RCP and the members 60 thereof are
13 designed to reflect LCP, logic trees 1 provide an image at their imaging cells 71 in the
14 same way described in connection with FIG. 1 and the resulting outputs will have
15 polarizations like those shown in FIG. 1. The polarizations at STAGE 3 for each of logic
16 trees 1 are:

17
18 RLRL RLRL

19
20 To obtain this result, however, input logic tree 72 must provide RCP at all its imaging
21 cells 71. This requires an RCP input from laser 71, a logic tree with elements which
22 reflect LCP and fixed half-wave retarders 63 (not shown) disposed after imaging cells 71
23 which provide LCP outputs.

24
25 To obtain a single polarization for all of the outputs of first and alternate logic trees 1 of
26 array 70, for example, RCP, the LCP outputs of these logic trees 1 must be converted to
27 RCP. This is accomplished by interposing fixed half-wave retarders 63 over the imaging
28 cells 71 having LCP outputs.

1 Similarly, to obtain a single but opposite polarization for all of the outputs of the second
2 and alternate logic trees 71, for example, LCP, the RCP outputs of these logic trees 1
3 must be converted to LCP. This is accomplished by interposing fixed-half wave retarders
4 63 over the imaging cells 71 having RCP outputs.

5
6 At this point, two stereo-displaced images appear at the output imaging cells 71 of array
7 70. One image has an RCP polarization while the other has an LCP polarization. Then,
8 using glasses which have one lens which passes RCP and another lens which passes LCP,
9 a 3-D image is perceived by a viewer.

10
11 In connection with the 3-D embodiment of FIG 5, it should be appreciated that outputs
12 from stereo camera 73 may be in either digital or analog form. If the former, the digital
13 signals may be converted to analog signals using a digital-to-analog converter in a well-
14 known way. Also, to the extent that logic trees 1 are provided with signals representing a
15 scanned line of an image and a stereo displaced image, these signals are arranged to
16 alternately access alternate ones of logic trees 1 in succession until two stereo displaced
17 images are formed at the imaging cells 71 of array 70. The scanned lines of an image and
18 a stereo displaced image are electronically interlaced so that source 17 is modulated first
19 by signals representing a scanned image and then by signals representing a scanned stereo
20 displaced image and so on in succession until the two images are formed.

21
22 From FIG. 5, it can be seen that, for a 3-D array, two 4x8 interleaved arrays are required,
23 one for an image and another for a stereo displaced image. Extrapolating this information
24 to a practical level, if 1024 imaging cells are wanted for each image, an array of
25 2048x1024 imaging cells would be required. Using the same approach as demonstrated
26 by FIG. 5, two 512 x 1024 interleaved arrays may be used with the sacrifice of some
27 resolution. In FIG. 5, logic trees 1 have been interleaved horizontally for ease of
28 fabrication but, they may be interleaved vertically without departing from the spirit of the
29 present application.

1 Referring now to FIG. 6, there is shown an orthographic, cut-away projection of a
2 plurality of layers 80 of insulating material, like SiO_2 , polycarbonate, acrylic or any other
3 appropriate optically transparent material, and a plurality of layers 81 of cholesteric liquid
4 crystal (CLC) material interleaved with layers 80.

5
6 In FIG. 6, layers 80, 81 are subjected to a slicing operation which cuts into layers 80, 81
7 at an angle, preferably 45° . Layers 80, 81 may be cut by saws, lasers, jets or other
8 appropriate tool to provide layers 82 which contain CLC members 60 disposed at an
9 angle of 45° in insulating material as shown in FIG. 7.

10
11 FIG. 7 is a cross-sectional view of a layer of insulating material in which CLC members
12 60 are disposed at an angle of 45° . The spacing of CLC members 60 is determined by
13 controlling the thicknesses of insulating layers 80 prior to the slicing step of FIG. 6.
14 Since alignment of CLC members 60 is important in transmitting electromagnetic energy
15 from stage-to-stage the spacing of members 60 must be carefully controlled. Thus, in
16 FIG. 7, the spacing between CLC members 60 is t units and could comprise STAGE 1,
17 for example, of array 70 of FIG. 4.

18
19 FIG. 8 is a cross-sectional view of a layer of insulating material in which members 60 are
20 disposed at an angle of 45° and is similar to FIG. 7 except that members 60 are spaced
21 apart by $t/2$ units. Layer 82 and other like layers are fabricated by slicing an arrangement
22 like that shown in FIG. 6 except that the thicknesses of layers 80 of insulating material
23 are reduced to half that shown in FIG. 6. After slicing a stack like that shown in FIG. 6,
24 the resulting layer 82 with a spacing of $t/2$ between members 60 could comprise stage 2,
25 for example, of array 70 of FIG. 4.

26
27 FIG. 9 is a cross-sectional view of a layer of insulating material in which members 60 are
28 disposed at an angle of 45° and is similar to FIG. 7 except that members 60 are spaced
29 apart by $t/4$ units. Layer 82 in FIG. 9 is fabricated by slicing an arrangement like that
30 shown in FIG. 6 except that the thicknesses layers 80 would be reduced to one-quarter

1 that shown in FIG. 6. After slicing a stack like that shown in FIG. 6, the resulting layer
2 82 with a spacing of $t/4$ between members 60 could comprise STAGE 3, for example, of
3 array 70 of FIG. 4.

4
5 The spacing of members 60 is always reduced by half as additional stages are added so
6 that higher and higher resolutions may be obtained. Thus, for an array with ten stages,
7 the spacing between CLC members 60 would be $t/512$ units.

8
9 By slicing arrangements like that shown in FIG. 6 and controlling the thicknesses of
10 layers 80, layers 82 with members 60 spaced apart by different amounts like those shown
11 in FIGS 7-9 may be easily obtained. As will be seen below, layers 82 with appropriately
12 spaced members 60 may be stacked to produce an array 70 like that shown in FIG. 4 or
13 an array having as many stages as desired. This can be done on a mass-production basis
14 to produce literally thousands of layers like layers 82 of FIGS. 7-9.

15
16 FIG. 10 is a cross-sectional, orthographic projection of a layer 82 which contains CLC
17 members 60 disposed at an angle of 45° therein. Layer 82 in FIG. 10 is similar to layer
18 82 of FIG. 8 except that in FIG. 10, a ground plane 83 is deposited or formed on the
19 bottom of layer 82. Layer 83 is transparent and metallic in character and acts as a ground
20 plane for subsequently deposited electrodes which activate variable half-wave retarders
21 61. A material like indium-tin oxide (ITO) may be deposited or formed in a well-known
22 way on the bottom of layer 82 of FIG. 10. The transparency of ITO, of course, permits
23 the transmission of light energy from stage-to-stage with little or no loss in intensity.

24
25 Referring to FIG. 11, there is shown a cross-sectional, orthographic projection similar to
26 FIG. 10 except that electrodes 84 are shown disposed over every other CLC member 60,
27 like they would be if layer 83 of FIG. 11 were to be utilized as a STAGE 2 in an array 70
28 like that shown in FIG. 4. This pattern of electrode spacing will always be the same
29 regardless of which stage is being considered. A reconsideration of FIG. 1 shows this to
30 be true since each stage always comprises at least one branch consisting of active and

passive CLC elements. Electrode 84 (62 in FIG.1) is always associated with and forms a part of variable half-wave retarders 61 which, in turn, is always associated with the active CLC element of any branch. Like ground plane 83, electrode 84 is comprised of indium-tin-oxide (ITO) material which is transparent to the electromagnetic radiation being utilized. To obtain electrodes 84 in the form shown in FIG. 11, indium-tin oxide is formed atop layer 82 and, using well-known lithographic, masking and etching techniques, electrodes 84 are appropriately positioned over every other CLC member 60. Rather than carrying out two separate deposition steps for ground plane 83 and electrodes 84, the ITO material may be formed simultaneously on each side of layer 82. Then, the photolithographic, masking and etching steps are carried out.

Referring now to FIG. 12, there is shown a cross-sectional view of a layer 82 similar to that shown in FIG. 11 except that a spacer is added around the periphery of layer 82 and the thus enclosed volume is filled with a phase-shifter material in liquid form.

In FIG. 12, a spacer 85 is formed around the periphery of layer 82 by, for example, gluing a spacer 85 of insulating material around the edge of layer 82. Spacer 85 separates layers 82 from other overlying layers and defines the volume into which phase-shifter material 86 is placed.

FIG. 13 is a top view of a logic tree 1 made up of layers 82 like those shown in FIGS. 7-12. The arrangement of FIG. 13 shows the topmost logic tree 1 of FIG. 4 after it has been fabricated in accordance with the teaching of the present application. FIG. 13 can also be considered a side-view of input logic tree 72 since its structure does not depart in any way from the structure of logic tree 1.

One way of assembling the structure of FIG. 13, is to stack a finished layer 82 like that shown in FIG. 12 on a finished layer 82 like that shown at the bottom of FIG. 13. Another layer 82 like that shown at the top of FIG. 13 is stacked atop the finished layer 82 of FIG. 12. The layers are glued together with the topmost layer 82 forming STAGE 1

1 as shown in FIG. 4; the middle layer 82 forming STAGE 2 as shown in FIG. 4. and the
2 bottom layer 82 forming STAGE 3 as shown in FIG. 4. Thus, inputs provided to the
3 leftmost CLC member 60 of topmost layer 82 will, under control of inputs to electrodes
4 84 from pulsed source 27, appear as outputs emanating, from left-to-right, from CLC
5 members 60 of bottommost layer 82 as a scanned line of modulated or unmodulated light.

6
7 For the array, once stacked, the top and bottom thereof may be covered with insulating
8 layers, one of which contains holes which register with the ends of electrodes 84 and
9 ground planes 83. Thus, even when logic trees 1 are not being utilized, their associated
10 electrodes 61, 84 which extend from top-to-bottom of array 70 and are electrically
11 connected as shown in FIG. 5 are simultaneously energized.

12
13 Inputs to the stacked logic trees 1 are provided, as shown in FIG. 4, from imaging cells
14 71 of input logic tree 72. The orientation of input logic tree 72 with respect to array 70 is
15 best shown in FIG. 4 which does not depart in any way from the arrangement of FIG. 13.
16 The latter figure merely shows the structural details to better effect. Thus, as previously
17 explained in FIG. 4, outputs from imaging cells 71 of input logic tree 72 are scanned
18 from top-to-bottom of tree 72 and each output initially accesses the leftmost member 60
19 of its associated logic tree 1 such that outputs appear at imaging cells 71 of array 70 as a
20 plurality of left-to-right scans which go from the topmost logic tree 1 to the bottommost
21 logic tree 1 of array 70.

22
23 Input logic tree 72 may take the form of an array 70 rotated 90° so that imaging cells 71
24 there of register with the leftmost retarder 61 of each of the logic trees 1 like lasers 17 as
25 shown in FIG. 3. In this instance, only a single logic tree 1 of the rotated array 70 is
26 energized.

27
28 Alternatively, the array shown in FIG. 13 may be fabricated without introducing the
29 phase shifter material 86. The structure of FIG. 13 is then sliced in a direction parallel to
30 the surface thereof resulting in a structure similar to input logic tree 72 as shown in FIG.

4. The resulting slice is placed on an insulating layer and bonded to it. A cover layer of insulating material having holes therein which register with electrodes 84 and ground planes 83 is fabricated by drilling or etching using well-known photolithographic techniques. The volumes enclosed by the insulation layer are now filled with liquid phase shifter material 86. The cover layer is affixed to the other side of the logic tree slice. A metallic layer such as aluminum is then deposited on the surface of the cover layer and in the holes previously formed therein. Then, using well-known photolithographic masking and etching techniques, conductors to electrodes 84 in a ground planes 83 are formed

Without going into exhaustive detail, it should be appreciated that the side of input logic tree 72 of FIG. 4 may be butted against the back of array 70. In this way, the overall thickness of the arrangement of FIG. 4 is substantially reduced. Well-known optical techniques using reflectors may be used to apply a 90° turn to light emanating from imaging cells 71 of tree 72 when it is butted against the back of array 70.

Since electrodes 84 extend from front-to-back on each logic tree 1 as shown, for example, in FIG. 13, they are best accessed from the front or back of the array with activating metallic lines 29, as shown in FIG. 4, extending in insulated spaced relationship with a surface of array 70 to a plug which can be connected to pulsed source 27, for example. This may be accomplished using well-known photolithographic and etching techniques.

The arrangements shown in FIGS. 6-12 may have the following typical dimensions:

Layers 82	0.5 mm thick and up
Electrodes 84	500Å to 1000Å thick
Ground Planes 83	500 Å to 1000 Å thick
Spacer 85	1μ to 10μ thick
Elements 60	2μ to 30μ thick

Cells 71

0.5 mm wide and up. May exceed 10 cm

Typical voltages applied to electrodes 84 may range between 5V and 100V.

From the foregoing, it should be clear that arrays 70 may range in size from that typical of T.V. sets used in the home to displays similar to those used in stadia. The resulting arrays are flat, light weight, require but a single laser source or multiple laser sources and are inexpensive and easily fabricated.